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(54) Title: METHOD TO SECURE AN ELECTRONIC ASSEMBLY EXECUTING ANY ALGORITHM AGAINST ATTACKS BY ERROR INTRODUCTION

(57) Abstract: The invention concerns an automatic method to secure an electronic calculation assembly against attacks by error introduction or by radiation. The following are used: 1) Static information generated by the automatic process; 2) A dynamic part of the memory of the electronic system allocated by the automatic process; 3) Beacons and check points to mark out the code, introduced by the automatic process; 4) Beacon functions storing information in the dynamic memory; 5) History verification functions using the static information and the dynamic memory to check that no errors have been introduced.